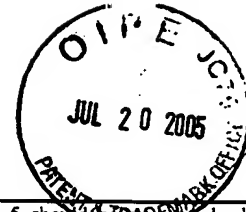


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7590 04/20/2005

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07/21/2005 MBEYENE2 00000080 10731152

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/731,152	12/10/2003	Koichiro Ishibashi		4808

TITLE OF INVENTION: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	07/20/2005

EXAMINER	ART UNIT	CLASS-SUBCLASS
FENTY, JESSE A	2815	257-391000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

2. For printing on the patent front page, list

- (1) the names of up to 3 registered patent attorneys or agents OR, alternatively,
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1. Mattingly, Stanger, Malur & Brundidge, P.C.
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(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Renesas Technology Corp.

Tokyo, Japan

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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Authorized Signature John R. Mattingly

Date July 20, 2005

Typed or printed name John R. Mattingly

Registration No. 30,293

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